



## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
 United States Patent and Trademark Office  
 Address: COMMISSIONER FOR PATENTS  
 P.O. Box 1450  
 Alexandria, Virginia 22313-1450  
[www.uspto.gov](http://www.uspto.gov)

## BIB DATA SHEET

CONFIRMATION NO. 1901

SERIAL NUMBER	FILING or 371(c) DATE RULE	CLASS	GROUP ART UNIT	ATTORNEY DOCKET NO.
10/647,217	08/26/2003	257 714	2117	SON-2810

**APPLICANTS**

Yoshitaka Kayukawa, Kanagawa, JAPAN;  
 Tetsuya Aoki, Tokyo, JAPAN;  
 Takahiro Hamaguchi, Kanawaga, JAPAN;  
 Noriyuki Oshima, Kanagawa, JAPAN;

**\*\* CONTINUING DATA \*\*\*\*\*****\*\* FOREIGN APPLICATIONS \*\*\*\*\***

JAPAN P2002-277285 09/24/2002

**\*\* IF REQUIRED, FOREIGN FILING LICENSE GRANTED \*\***

11/22/2003

Foreign Priority claimed	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	STATE OR COUNTRY	SHEETS DRAWINGS	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119(a-d) conditions met	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No				
Verified and	/DIPAKKUMAR B GANDHI/ Examiner's Signature	Met after Allowance Initials	JAPAN	4	12
Acknowledged					7

**ADDRESS**

RADER FISHMAN & GRAUER PLLC  
 LION BUILDING  
 1233 20TH STREET N.W., SUITE 501  
 WASHINGTON, DC 20036  
 UNITED STATES

**TITLE**

Semiconductor integrated circuit and method for testing same

<b>FILING FEE RECEIVED</b> 1236	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees
		<input type="checkbox"/> 1.16 Fees (Filing)
		<input type="checkbox"/> 1.17 Fees (Processing Ext. of time)
		<input type="checkbox"/> 1.18 Fees (Issue)
		<input type="checkbox"/> Other _____
		<input type="checkbox"/> Credit